

(12) PATENT APPLICATION PUBLICATION

(21) Application No.202511095612 A

(19) INDIA

(22) Date of filing of Application :05/10/2025

(43) Publication Date : 05/12/2025

(54) Title of the invention : AN IMPROVED CIRCUIT FOR LOW-NOISE WIRELESS COMMUNICATION RECEIVERS

(51) International classification	:H04B0001440000, H03F0001260000, H04B0007080000, H03F0001560000, H01Q0001420000	(71) <b>Name of Applicant :</b> <b>1)NOIDA INSTITUTE OF ENGINEERING &amp; TECHNOLOGY</b> Address of Applicant :19, Knowledge Park-II, Institutional Area, Greater Noida – 201306, Uttar Pradesh, India. Uttar Pradesh India
(31) Priority Document No	:NA	(72) <b>Name of Inventor :</b>
(32) Priority Date	:NA	<b>1)Dr. VIJAY KUMAR PANDEY</b>
(33) Name of priority country	:NA	<b>2)Dr. SARABJEET KAUR</b>
(86) International Application No	:	
Filing Date	:01/01/1900	
(87) International Publication No	: NA	
(61) Patent of Addition to Application Number	:NA	
Filing Date	:NA	
(62) Divisional to Application Number	:NA	
Filing Date	:NA	

(57) Abstract :

The invention relates to an improved circuit (100) for low-noise wireless communication receivers designed to enhance signal sensitivity and reduce interference. The circuit comprises an antenna input (101), an impedance-matching network (130), a low-noise amplifier (110), an adaptive filter (120), and a feedback noise suppression block (140). The amplifier (110) ensures high gain with minimal noise, while the filter (120) dynamically suppresses unwanted signals. The impedance-matching network (130) reduces reflection losses, and the feedback block (140) optimizes performance in real time. The invention provides high SNR, multi-band adaptability, and energy efficiency.

No. of Pages : 14 No. of Claims : 6